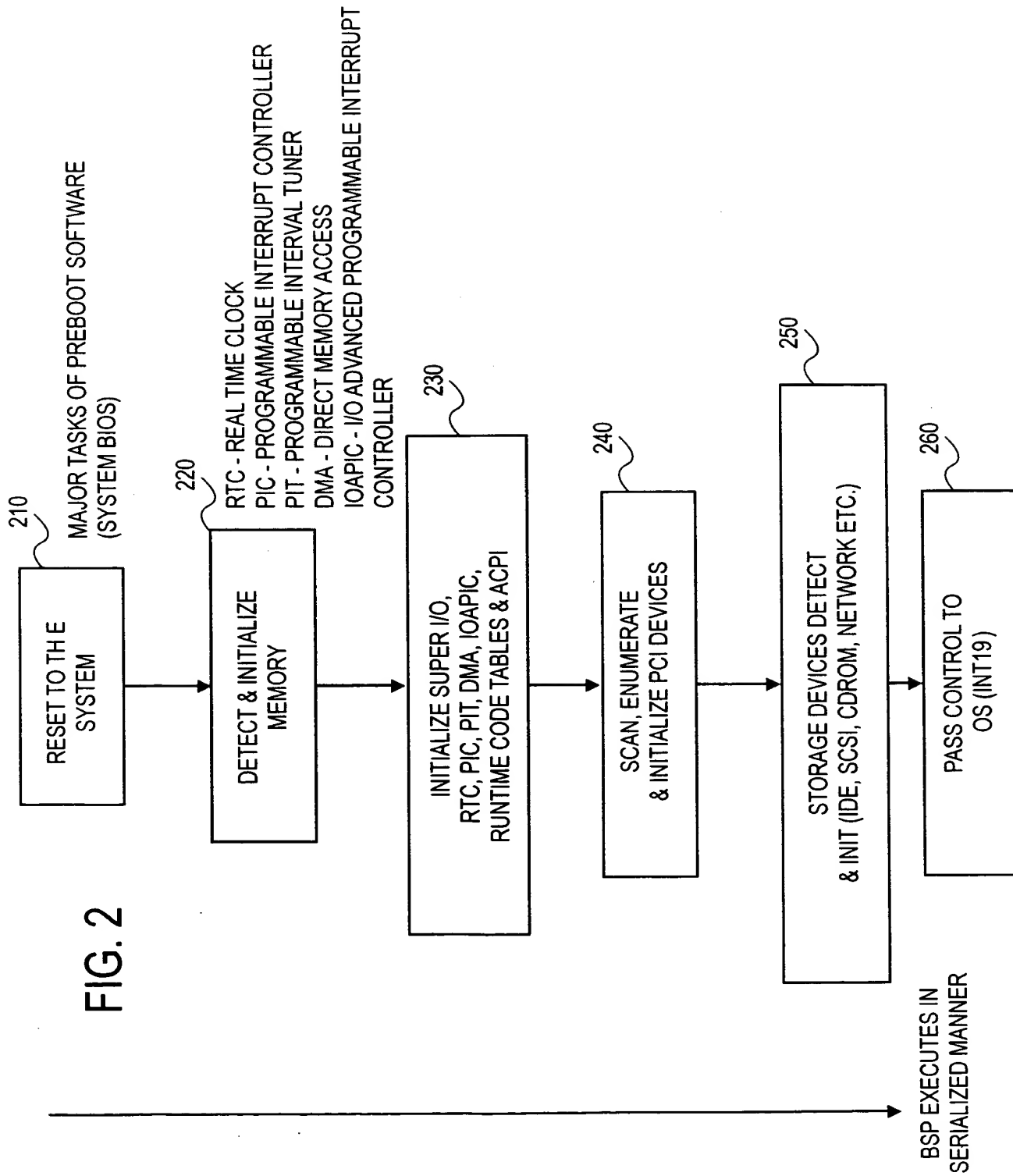


FIG. 2



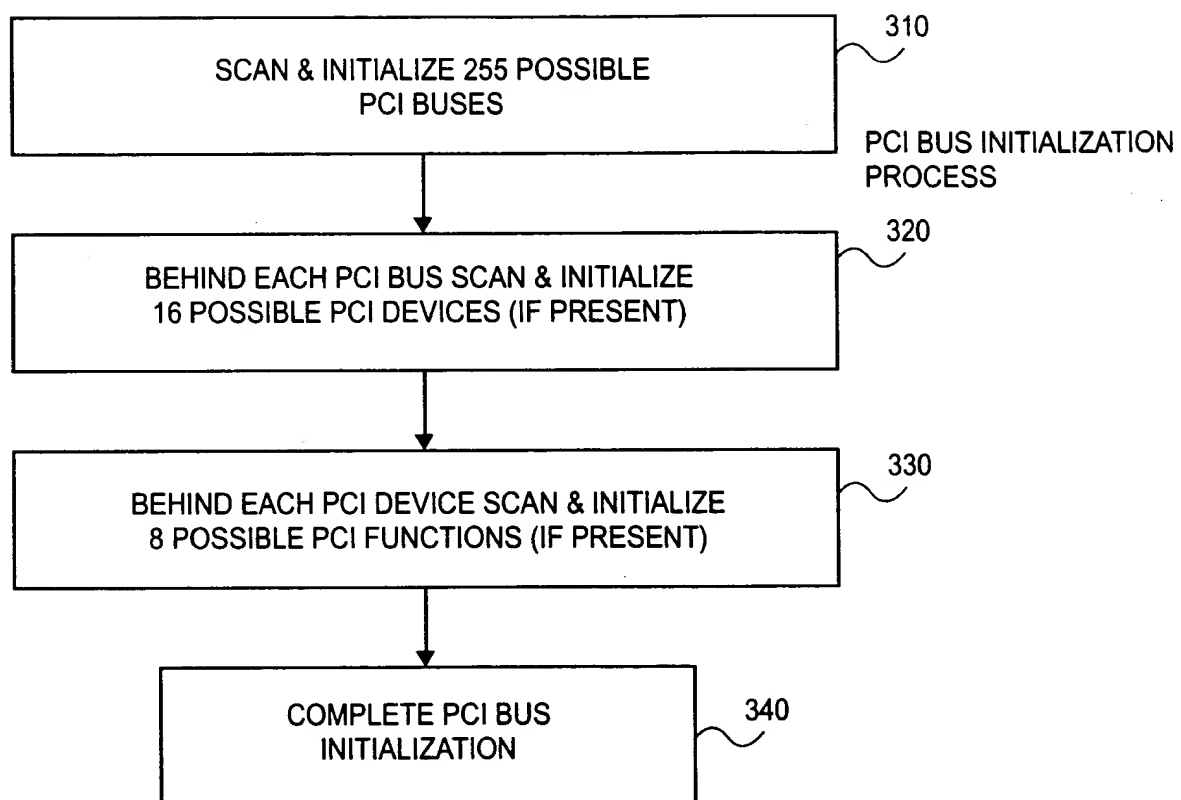


FIG. 3

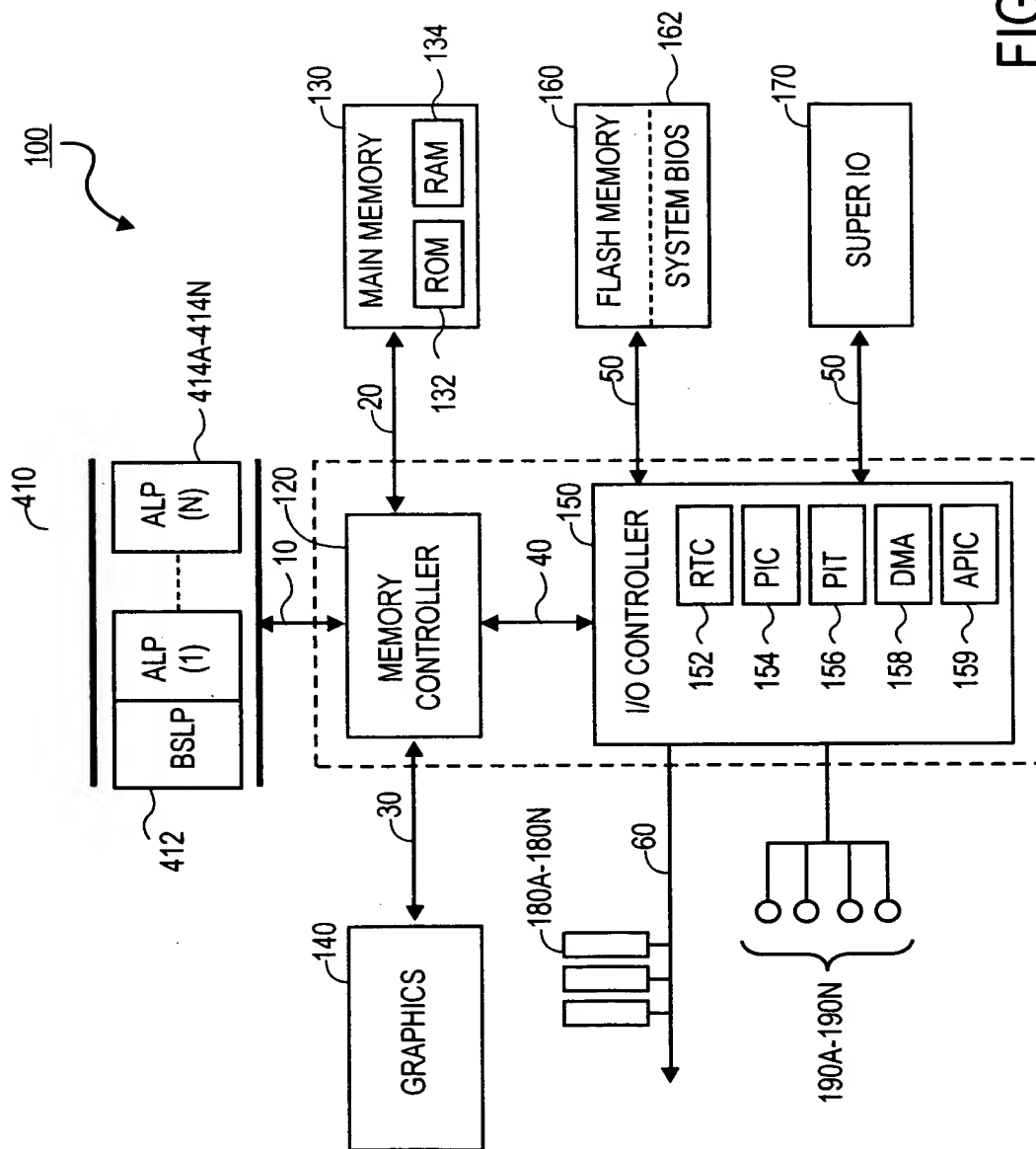


FIG. 4

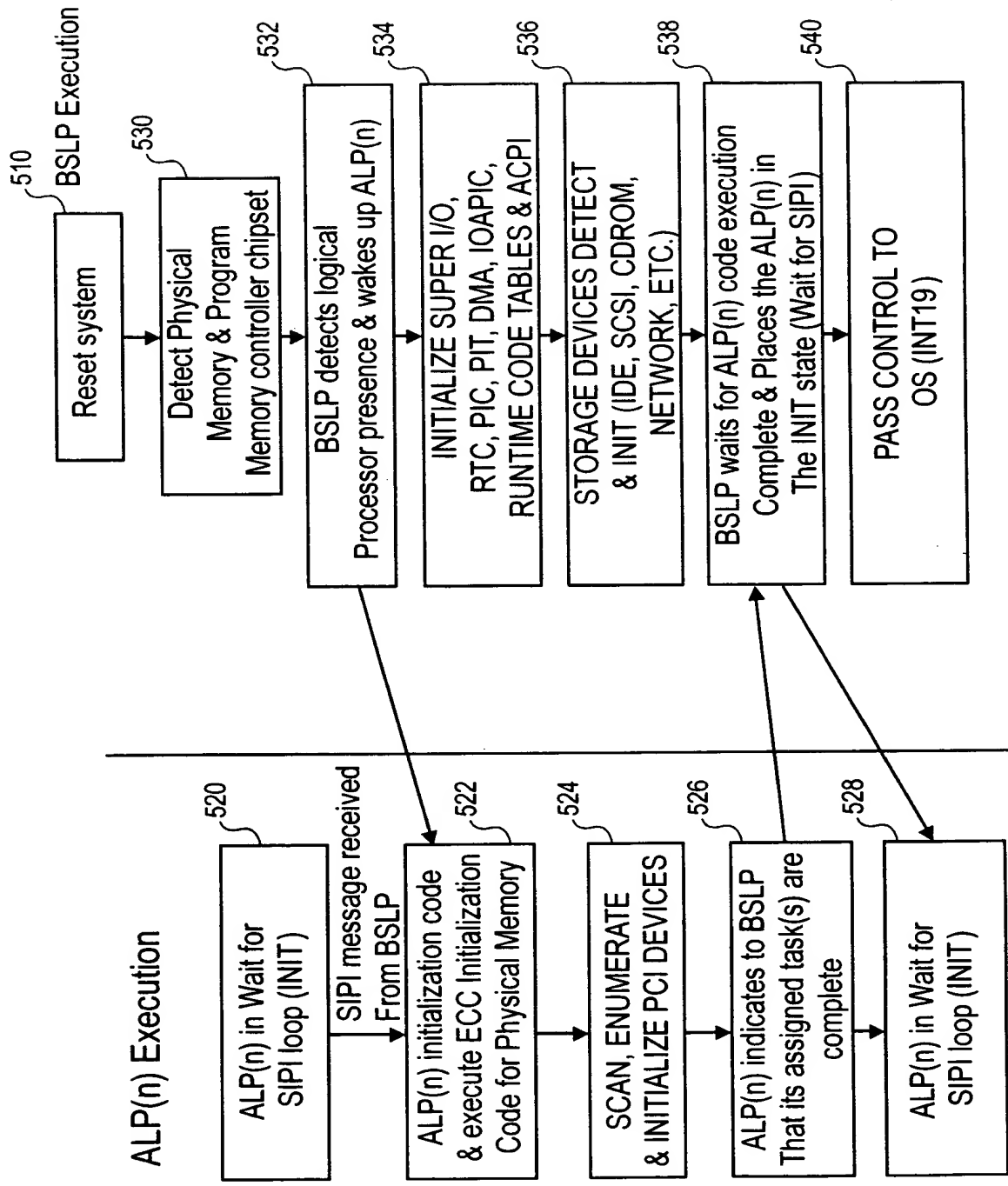


FIG. 5

Pre-boot software execution timeline

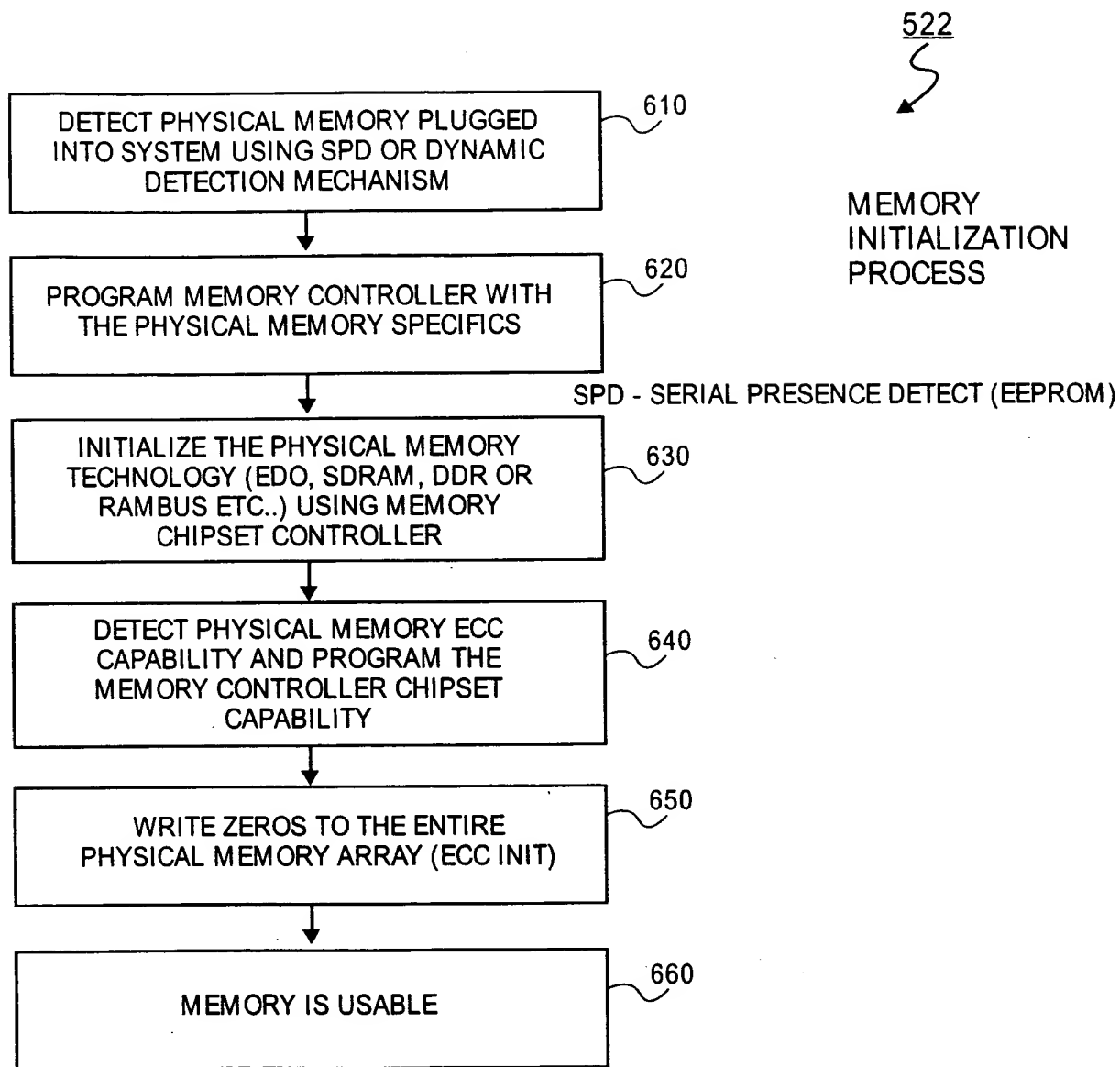


FIG. 6